

FIG. 1

200

204

202

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222
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A/D

203
210

Processor Complex

233
buffer

208

D/A

224

Non-audio
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217

I/O Channel
223

I/O
Adapter

EPROM
235

PIM instructions

230
Tables

Device-specific
logic device
214

FIG. 2
[Diagram of a device-specific logic device]

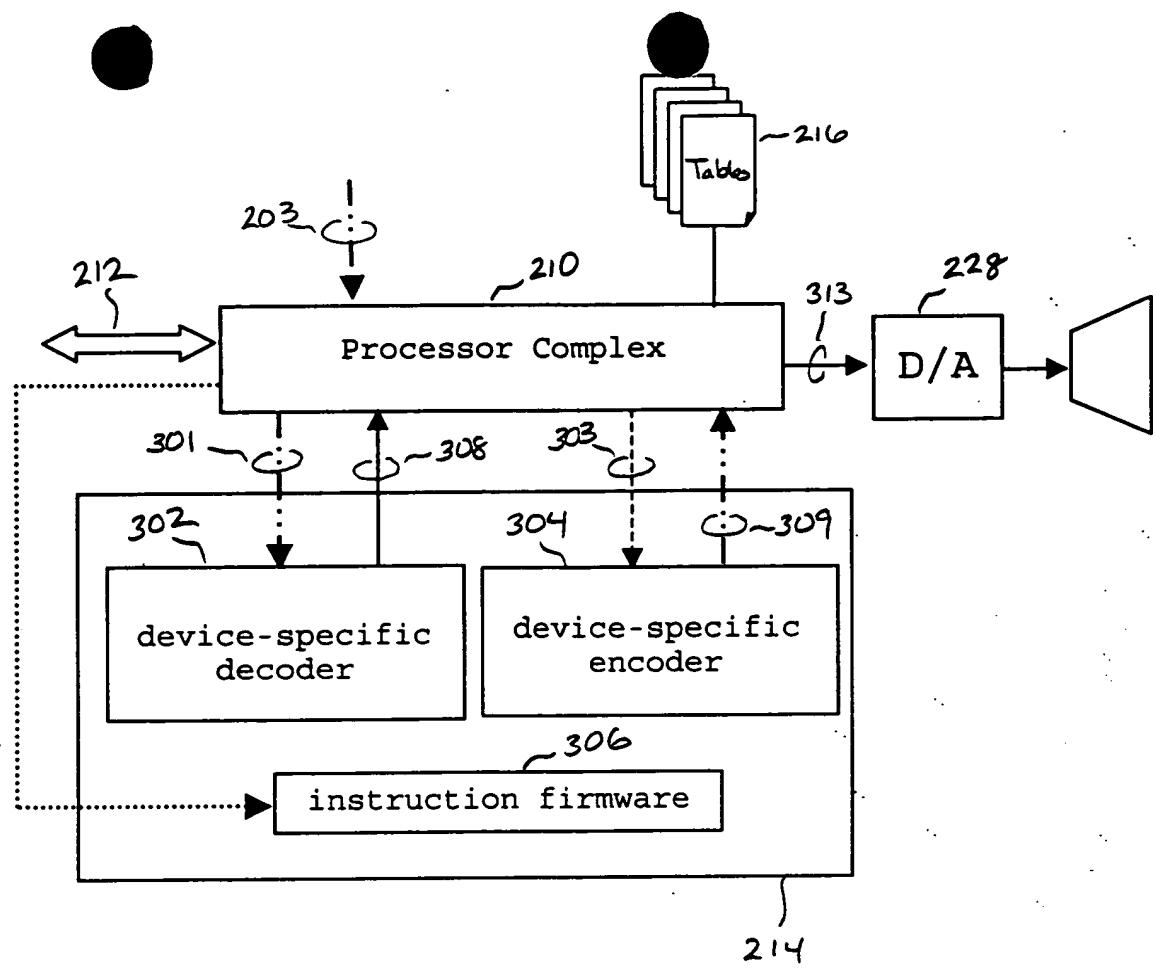


FIG. 3

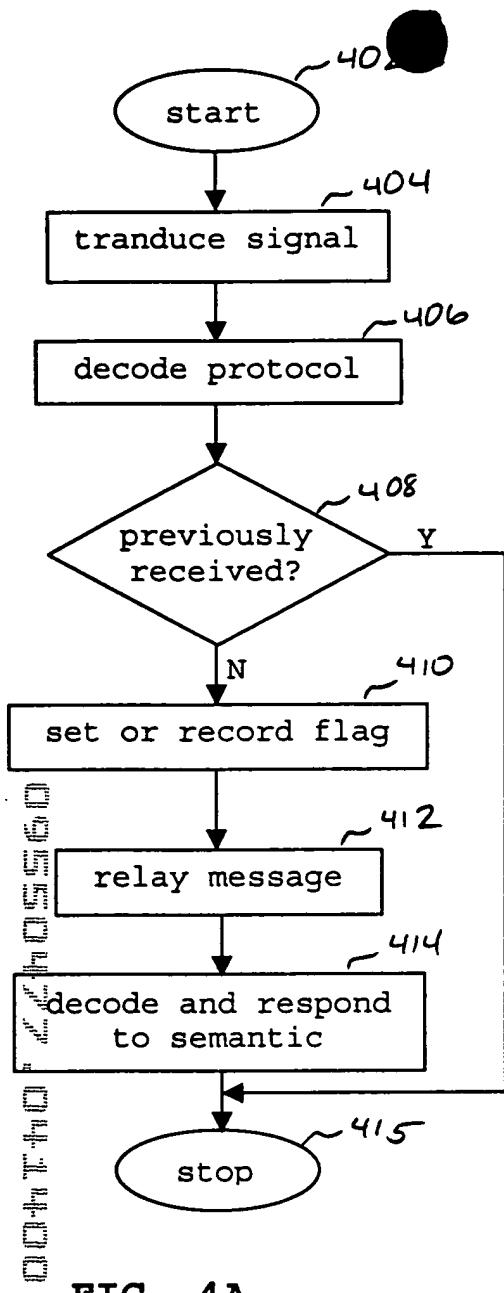


FIG. 4A

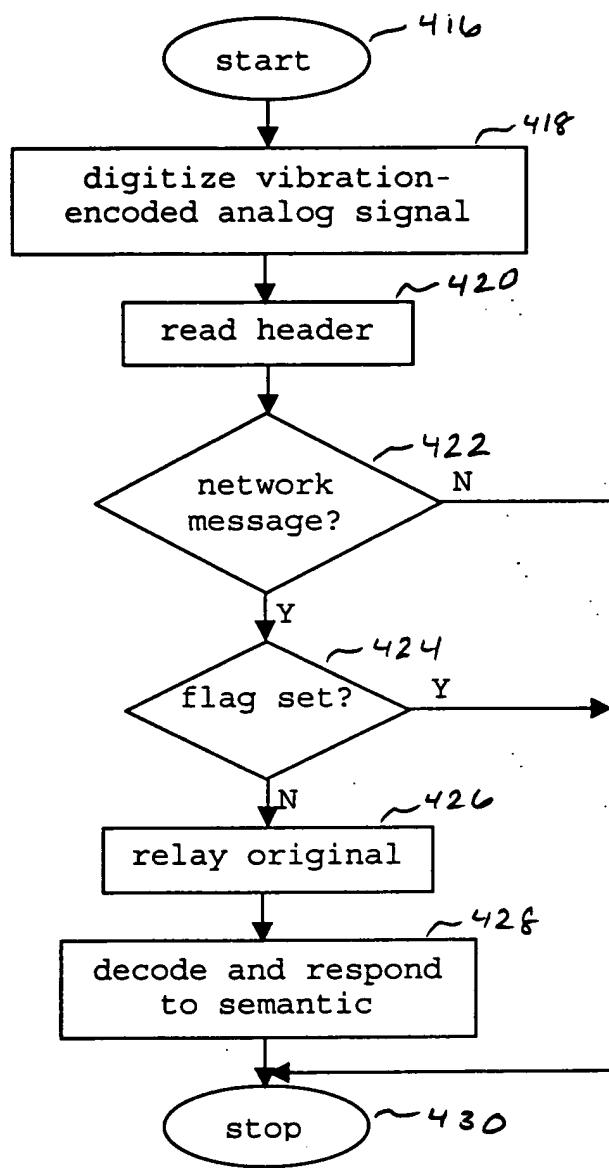


FIG. 4B

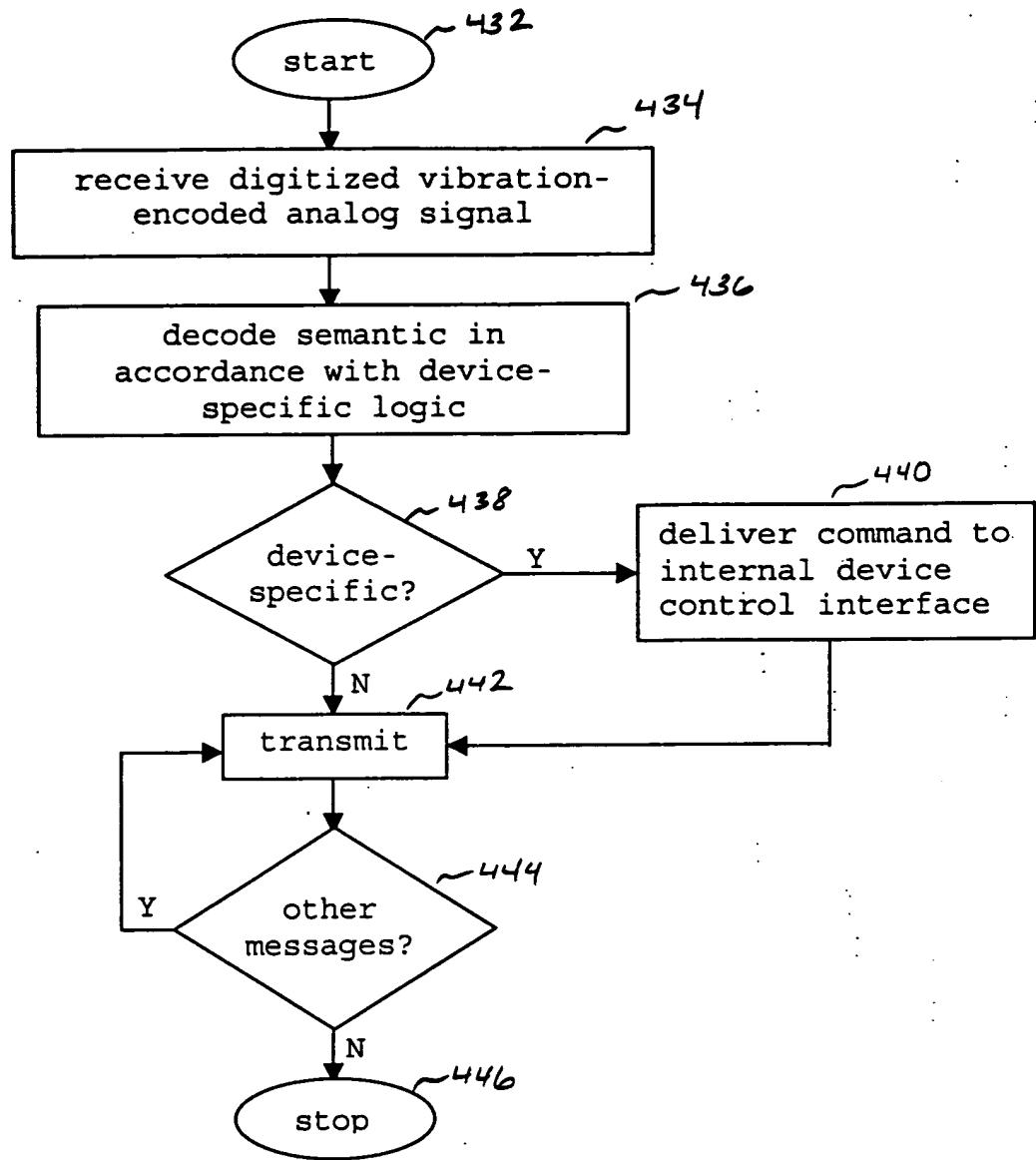


FIG. 4C